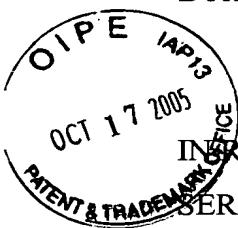


Docket No.: 241785US2X



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE APPLICATION: Satoshi YAMAMOTO, et al.

SERIAL NO.: 10/646,757

GAU: 1765

FILED: August 25, 2003

EXAMINER: Patricia Ann George

FOR: MANUFACTURING METHOD OF A SEMICONDUCTOR SUBSTRATE PROVIDED
WITH A THROUGH HOLE ELECTRODE

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted
herewith:

1 Replacement Drawing Sheets _____ New Drawing Sheets

Official Action dated July 15, 2005

Notice of Allowance/Issue Fee dated _____

Other dated _____

The changes and/or modifications made include the following:

The changes and/or modification made include the following:
Figs 1, 2A, 2B and 2C have been designated as --Prior Art--.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
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